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jc760 U.S. PTO

Atty. Dkt.: 925-151

Sir:

07/14/00

Date: July 14, 2000

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09/617120
07/14/00

Attached for filing is the patent application of:

Inventor: KATSUYA

Entitled: **LIQUID CRYSTAL DISPLAY DEVICE HAVING
HIGH LIGHT UTILIZATION EFFICIENCY**

and including attachments as noted below:

Declaration, Abstract
 29 pages of specification and claims (including 7 numbered claims), and
 5 sheets of accompanying drawings.
 Record & return the attached assignment to the undersigned.
 Priority is hereby claimed under 35 USC 119 based on the following foreign applications, the entire content of which is hereby incorporated by reference in this application:

Application Number	Country	Day/Month/Year Filed
11-206090	JAPAN	21 July 1999

, respectively.

Certified copy of foreign application is attached.
 Please amend the specification by inserting before the first line --This is a of PCT application , filed , the entire content of which is hereby incorporated by reference in this application.--
 Priority is hereby claimed under 35 USC 120/365 based on the following prior PCT applications designating the U.S., the entire content of which is hereby incorporated by reference in this application:

Application Number	Country	Day/Month/Year Filed
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This application is based on the following prior provisional application(s):
 Application No. Filing Date

respectively, the entire content of which is hereby incorporated by reference in this application, and priority is hereby claimed therefrom.

Please amend the specification by inserting before the first line: -- This application claims the benefit of U.S. Provisional Application No. , filed , the entire content of which is hereby incorporated by reference in this application.

Verified Statement attached establishing "small entity" status (Rules 9 & 27)
 The Examiner's attention is directed to the prior art cited in the parent application by applicant and/or Examiner for the reasons stated therein.
 Preliminary amendment to claims (attached hereto), to be entered before calculation of the fee below.

Also attached: **Information Disclosure Statement/ PTO-1449/ One Reference**

FILING FEE IS BASED ON CLAIMS AS FILED LESS ANY HEREWITH CANCELED

Basic Filing Fee	\$	690.00
Total effective claims 7 - 20 (at least 20) = 0 x \$ 18.00	\$	0.00
Independent claims 1 - 3 (at least 3) = 0 x \$ 78.00	\$	0.00
If any proper multiple dependent claims now added for first time, add \$260.00 (ignore improper)	\$	0.00
SUBTOTAL		\$ 690.00
If "small entity," then enter half (1/2) of subtotal and subtract	\$	(0.00)
SECOND SUBTOTAL		\$ 690.00
Assignment Recording Fee (\$40.00)	\$	40.00
TOTAL FEE ENCLOSED		\$ 730.00

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By Atty: H. Warren Burnam, Jr., Reg. No. 29,366

Signature: H. Warren Burnam, Jr.

Our Ref.: 925-151
531454/HY/kt

U.S. PATENT APPLICATION

Inventor(s): Yoko KATSUYA

Invention: LIQUID CRYSTAL DISPLAY DEVICE HAVING HIGH LIGHT UTILIZATION EFFICIENCY

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SPECIFICATION

LIQUID CRYSTAL DISPLAY DEVICE HAVING
HIGH LIGHT UTILIZATION EFFICIENCY

BACKGROUND OF THE INVENTION

5 The present invention relates to a liquid crystal display device that employs a thin film transistor array substrate provided with a supplementary capacitance for each pixel.

In recent years, as a thin type light-weight
10 display having a low power consumption, an active matrix type liquid crystal display device in which each pixel electrode is controlled by a semiconductor element such as a thin film transistor provided for each pixel electrode has attracted a great deal of attention among, in particular,
15 liquid crystal display devices, since the device can obtain an excellent resolution and a vivid image. Reference will be made below to the active matrix type liquid crystal display device.

As a semiconductor element for use in the
20 conventional active matrix type liquid crystal display device, a thin film transistor constructed of an amorphous silicon thin film has been known, and a great many active matrix type liquid crystal display devices mounted with the thin film transistors are currently produced on the
25 commercial basis. The active matrix type liquid crystal

display device is about to go mainstream as a display for Office Automation equipment and commercial equipment.

With regard to the active matrix type liquid crystal display device, a transmission type liquid crystal display device that employs a transparent conductive thin film of ITO (Indium Tin Oxide) or the like for pixel electrodes is general. In the active matrix type liquid crystal display device, a capacitor is constructed by holding a liquid crystal layer between the pixel electrode and an opposite electrode provided on the opposite substrate side, and display is effected by retaining the electric potential of the pixel electrode at a predetermined voltage for a specified period in correspondence with an image signal. However, the display may be deteriorated by the potential reduction of the pixel electrode due to the electric discharge of the capacitor ascribed to a leak current or the like when the pixel use thin film transistor is turned off. It is sometimes the case where the pixel potential might fluctuate under the influence of the potentials of the neighbor wiring. Accordingly, in order to prevent the above-mentioned potential fluctuation of the pixel electrode, a supplementary capacitance is normally formed parallel to the capacitor.

The conventional supplementary capacitance has often been formed by using a gate insulating film as a

dielectric film for forming the supplementary capacitance, using a capacitive wiring formed on a layer identical to that of a gate wiring or the gate wiring as one electrode and holding the dielectric film between the one electrode 5 and a drain electrode or a pixel electrode. The reason for the above is that the dielectric film can be formed concurrently with the fabrication of the thin film transistor and that a gate insulating film having a good quality can be utilized for the dielectric film.

10 As a liquid crystal display device in which the supplementary capacitance is formed by the fabricating method described above, there is one shown in Fig. 7. As shown in Fig. 7, this liquid crystal display device has a polysilicon 52 that is formed on a transparent substrate 51 and patterned in an island-like shape, a gate insulating film 53 formed on the polysilicon 52, a gate electrode 54 and a common electrode 55 formed on the gate insulating film 53, a first interlayer insulating film 58 formed on the gate insulating film 53, the gate electrode 54 and the common 15 electrode 55, a drain electrode 56 and a source electrode 57 formed on the first interlayer insulating film 58, a second interlayer insulating film 59 formed on the first interlayer insulating film 58, the drain electrode 56 and the source electrode 57, a transparent conductive film 61 formed on the 20 second interlayer insulating film 59 and a pixel electrode 25

62 that is formed on the transparent conductive film 61 and is electrically connected to the drain electrode 56 at a contact hole 60. The supplementary capacitance is formed by using the gate insulating film 53 as a dielectric film and 5 holding the gate insulating film 53 between the common electrode 55 and the polysilicon 52 (a region located on the connected drain electrode 56 side).

However, the aforementioned liquid crystal display device uses the gate insulating film 53 directly as a dielectric film for forming the supplementary capacitance. 10 Therefore, although the fabricating method becomes relatively simple, it is often the case where the film thickness and the like of the gate insulating film are restricted to a certain extent in order to assure the 15 performance of the thin film transistor. Due to this restriction, it has not been easy to concurrently assure the performances required for the gate insulating film and the dielectric film. Also, the capacitive wiring is formed in the same layer as that of the gate wiring, and therefore, it 20 is difficult to secure a capacitive electrode area for forming a sufficient supplementary capacitance in assuring the processing accuracy of a photolithographic process and an etching process or the aperture rate of the pixel. The above fact has tended to be more significant as the 25 processing dimensions of the thin film transistor become

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smaller, i.e., as the panel has a higher resolution with the dimensional reduction of the pixels. As described above, the conventional liquid crystal display device has the problem that the formation of the sufficient supplementary 5 capacitance is very hard to achieve according to the improvement in the resolution of the liquid crystal display device.

Furthermore, the liquid crystal display device generally receives the influence of the electric fields of 10 the adjacent electrodes at the boundaries of the pixel electrodes and the influence of the electric field of the bus line. This might cause the display of an image different from the image intended to be displayed on the pixel or the leak of light due to the absence of an electric 15 field. Therefore, it is not appropriate to use the boundary portions of mutually adjoining pixel electrodes for display, and normally these portions are shielded by a black matrix. Conventionally, the black matrix has been required to be constructed of another layer. For example, Japanese Patent 20 Laid-Open Publication No. HEI 5-216067 and so on propose the use of the bus line as a black matrix. However, in practice, there is the problem that the display becomes unstable under the influence of a bus line signal exerted on the pixel electrodes.

SUMMARY OF THE INVENTION

Accordingly, the object of the present invention is to provide a liquid crystal display device that can obtain high light utilization efficiency and a sufficient 5 supplementary capacitance without reducing the aperture rate of the pixels and has a good display quality capable of achieving the higher resolution.

In order to achieve the aforementioned object, the present invention provides a liquid crystal display device 10 comprising a thin film transistor array substrate including: pixel use thin film transistors, which are formed on an insulating substrate and each of which has a gate electrode, a source electrode and a drain electrode; pixel electrodes, which are formed on the insulating substrate and comprised 15 of a transparent conductive film connected to the respective pixel use thin film transistors; and a supplementary capacitance for retaining electric charges of the pixel electrodes, and a liquid crystal layer held between the thin film transistor array substrate and an opposite substrate, 20 the supplementary capacitance being provided by the pixel electrodes, a supplementary capacitance use transparent insulating film formed under at least the pixel electrodes and a common electrode that is formed under the supplementary capacitance use transparent insulating film

and comprised of a transparent conductive film connected to a specified potential, and

the pixel electrodes, the supplementary capacitance use transparent insulating film and the common 5 electrode having a film thickness such that the electrodes and film have a transmittance increased by interference at a specified wavelength.

According to the liquid crystal display device constructed as above, the potential of the pixel electrode applied by the pixel use thin film transistor is retained by the capacitance formed by holding the liquid crystal layer between the pixel electrode and an opposite electrode provided on the opposite substrate side. Furthermore, the auxiliary electrode constructed of the pixel electrode, the 10 supplementary capacitance use transparent insulating film and the common electrode prevents the fluctuation in potential of the pixel electrode ascribed to a leak current or the like when the pixel use thin film transistor is turned off and the potentials of the neighbor wiring. Both 15 the common electrode and the pixel electrode are formed of transparent conductive films (ITO or the like). This arrangement does not become a factor for obstructing the display, and the aperture rate is highly retained. The pixel electrode and the common electrode made of the 20 transparent conductive film have a refractive index greater 25

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than that of the glass substrate or the like. Therefore, depending on the film forming conditions, the films become the films for absorbing the light in the short wavelength region, while reflection tends to occur on the interfaces of 5 the pixel electrode and the common electrode due to the fact that the refractive index is different from those of the glass substrate and the silicon oxide film, often causing a reduction in light utilization efficiency. Therefore, the film thicknesses of the pixel electrode and the common 10 electrode are optimized so that the transmittance of light becomes high at the specified wavelength, and the film thickness of the supplementary capacitance use transparent insulating film is also optimized so that the transmittance of light becomes high at the specified wavelength. With 15 this arrangement, the pixel electrode, the supplementary capacitance use transparent insulating film and the common electrode are formed into high-quality films that absorb a small quantity of light. This arrangement can restrain the loss of the transmission light even if the common electrode 20 is formed roughly on the entire surface. This arrangement thus allows the supplementary capacitance to be increased without reducing the aperture rate of the pixel and optimizes not only the physical aperture rate but also the transmittance at the opening, consequently increasing the 25 light utilization efficiency. Therefore, a high light

utilization efficiency and a sufficient supplementary capacitance can be obtained without reducing the aperture rate even in a high-definition panel, so that a liquid crystal display device having a high display quality can be
5 provided.

In one embodiment, a difference between a refractive index of the supplementary capacitance use transparent insulating film and a refractive index of the pixel electrodes is set to a value of not greater than 0.6
10 and a difference between a refractive index of the supplementary capacitance use transparent insulating film and a refractive index of the common electrode is set to a value of not greater than 0.6.

According to the liquid crystal display device of
15 the above embodiment, the refractive index differences are each not greater than 0.6, and therefore, stabilized high light transmission characteristics can be obtained.

In one embodiment, the pixel electrode and the common electrode are made of a material having a specific
20 resistance of $1 \text{ m}\Omega\cdot\text{cm}$ or less.

According to the liquid crystal display device of
the above embodiment, the pixel electrode and the common
electrode constructed of the transparent conductive film
exert influence on the display as a consequence of the
25 generation of a potential difference in the planes thereof

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if their resistances are high. However, by virtue of the formation of the pixel electrodes and the common electrode roughly in the entire plane of the screen, their resistances matter less than when the electrodes are formed in linear shapes. However, a material having a resistance of not greater than 1 mΩ·cm is preferable.

In the liquid crystal display device of one embodiment, the pixel electrodes have edge portions overlapping gate bus lines and source bus lines formed on the insulating substrate, and

the common electrode is arranged between the gate bus lines and the pixel electrodes and between the source bus lines and the pixel electrodes so as to cover the gate bus lines and the source bus lines.

According to the liquid crystal display device of the above embodiment, the edge portions of the pixel electrodes are made to overlap the source bus lines and the gate bus lines, so that the boundaries of mutually adjoining pixel electrodes are provided on the source bus lines and the gate bus lines. With this arrangement, the source bus lines and the gate bus lines can be used as a black matrix. This arrangement obviates the need for separately forming a black matrix and is able to eliminate the black matrix forming process and reduce the cost. Furthermore, the common electrode is arranged between the gate bus lines and

the pixel electrodes and between the source bus lines and the pixel electrodes so as to cover the gate bus lines and the source bus lines. With this arrangement, electric fields attributed to the bus line signals are shielded by 5 the common electrode, by which the potentials of the pixel electrodes become hard to receive the influence of the bus line signals, allowing a good display quality to be obtained.

In the liquid crystal display device of one 10 embodiment, the supplementary capacitance use transparent insulating film is any one of a silicon oxide film, a silicon nitride film and an organic resin film or a laminate film comprised of at least two of the silicon oxide film, the silicon nitride film and the organic resin film.

15 According to the liquid crystal display device of the above embodiment, if the flat insulating layer is formed of any one of the silicon oxide film and a silicon nitride film, which are inorganic materials, and an organic resin film (acrylic resin, polyimide or the like) by the spin 20 coating method or the like as an insulating material between the common electrode and the pixel electrode for the formation of the supplementary capacitance, then the surface unevenness is reduced, which is effective in terms of uniforming the electric fields to be applied to the liquid 25 crystal molecules. Furthermore, by forming the

supplementary capacitance use transparent insulating film of a lamination of at least two of the silicon oxide film, the silicon nitride film and the organic resin film, both the dielectric properties and flatness can be secured.

5 In the liquid crystal display device of one embodiment, the pixel use thin film transistor has an active layer made of polysilicon, and

10 drive circuit use thin film transistors whose active layers are made of polysilicon are formed on the insulating substrate identical to the substrate on which the pixel use thin film transistors are formed.

15 According to the liquid crystal display device of the above embodiment, the pixel use thin film transistor and the drive circuit use thin film transistor, of which the active layers are made of polysilicon, have a mobility higher than that of the amorphous silicon thin film employed in the conventional thin film transistor. Therefore, a high-performance thin film transistor can be formed within a small area. Therefore, the pixel use thin film transistor and the drive circuit use thin film transistor for driving the pixel use thin film transistor can be integrally formed on one inexpensive glass substrate. This obviates the need for separately providing a substrate for the drive circuit section constructed of an IC (Integrated Circuit) and an LSI (Large Scale Integrated Circuit), allowing the manufacturing

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cost to be remarkably reduced by comparison with the conventional case. Therefore, a low-cost liquid crystal display device that employs a thin film transistor array substrate integrated with a drive circuit can be provided.

5 In the liquid crystal display device of one embodiment, the active layers of the pixel use thin film transistors and the drive circuit use thin film transistors are polysilicon films crystallized by utilizing a catalytic effect of an introduced catalytic element.

10 According to the liquid crystal display device of the above embodiment, by introducing a catalytic element (nickel, for example) and using the polysilicon film crystallized by utilizing its catalytic effect for the active layer of the thin film transistor, the drive circuit 15 section can be easily formed within a small area even in a high-definition panel.

BRIEF DESCRIPTION OF THE DRAWINGS

20 The present invention will become more fully understood from the detailed description given hereinbelow and the accompanying drawings which are given by way of illustration only, and thus are not limitative of the present invention, and wherein:

Fig. 1 is a schematic plan view of the thin film transistor array substrate of a liquid crystal display device according to one embodiment of the present invention;

Fig. 2 is a sectional view taken along the line 5 II-II in Fig. 1;

Fig. 3 is a sectional view taken along the line III-III in Fig. 1;

Fig. 4 is a view showing a relation between the refractive index and the transmittance of a transparent 10 insulating film for a supplementary capacitance;

Fig. 5 is a view showing a relation between the refractive index and the transmittance of the transparent insulating film depending on the film thickness;

Fig. 6 is a schematic plan view of a thin film 15 transistor array substrate with a dimensionally increased source electrode; and

Fig. 7 is a sectional view of essential part of the thin film transistor array substrate of a conventional liquid crystal display device.

20

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

The liquid crystal display device of the present invention will be described below on the basis of the embodiment thereof shown in the drawings.

Fig. 1 is a schematic plan view of the thin film transistor array substrate of a liquid crystal display device according to one embodiment of the present invention, showing a planar type thin film transistor array of the thin film transistor array substrate that employs polysilicon.

As shown in Fig. 1, according to the thin film transistor array substrate, a plurality of gate bus lines 1, a plurality of signal lines 2 formed perpendicularly to the plurality of gate bus lines 1 and a plurality of pixel electrodes 3 arranged so that their edges overlap the gate bus lines 1 and the signal lines 2 are formed on a transparent substrate 11. The transparent substrate 11 is made of aluminoborosilicate glass having a high heat resistance. Although Fig. 1 shows 3×3 pixel electrodes for the sake of providing a clear view, the number of pixel electrodes is not limited to this. A liquid crystal display device is constructed by holding a liquid crystal layer (not shown) between the thin film transistor array substrate and an opposite substrate (not shown) having an opposite electrode.

Fig. 2 is a sectional view taken along the line II-II in Fig. 1, while Fig. 3 is a sectional view taken along the line III-III in Fig. 1.

As shown in Fig. 2 and Fig. 3, a base coat layer (not shown) is first formed on the transparent substrate 11,

and after growing a polysilicon film on the base coat layer, the polysilicon film is patterned in an island-like shape to form a polysilicon 12. The polysilicon 12 is grown in a crystal form with nickel introduced as a catalyst.

5 Subsequently, an SiO_2 film is formed to a thickness of about 100 nm on the transparent substrate 11 and the polysilicon 12 by the CVD (Chemical Vapor Deposition) method, forming a gate insulating film 13. The gate insulating film 13 may be formed by the plasma CVD method, the atmospheric pressure

10 CVD method or the sputtering method.

Next, the gate bus lines 1 and the signal lines 2 (shown in Fig. 1) are formed of an Al alloy on the gate insulating film 13, and gate electrodes 14 connected to the gate bus lines 1 are formed. A silicon oxide film 15 is

15 formed as a first interlayer insulating film on the gate bus lines 1 and the gate electrodes 14. Next, drain electrodes 16 and source electrodes 17 are formed of a metal to a thickness of about 700 nm, and thereafter a flattening use resin film 18 is formed as a protecting film and a second

20 interlayer insulating film.

Subsequently, a transparent conductive film that serves as a common electrode 21 for the formation of a capacitance is formed to a thickness of about 140 nm, forming a silicon nitride film 22 that serves as a

25 supplementary capacitance use transparent insulating film to

a thickness of about 145 nm. The gate bus lines 1 and the signal lines 2 are subjected to taper etching for the prevention of disconnection. The common electrode 21 for the formation of the capacitance is formed almost on the 5 entire surface except for the contact forming portions (at and around contact holes 20).

Then, the contact holes 20 are formed through the flattening use resin film 18 and the silicon nitride film 22 located on the drain electrodes 16. Thereafter, a 10 transparent conductive film is formed to a thickness of 140 nm on the silicon nitride film 22 and the contact holes 20 and then patterned to form pixel electrodes 3. The pixel electrodes 3 are connected to the drain electrodes 16 via the contact holes 20. The pixel electrodes 3 are usually 15 made of ITO. Then, the common electrode 21 and the opposite electrode (not shown) are connected to each other outside the screen so as to have equal potential.

Assuming that the refractive index of the silicon nitride film is n and the wavelength at which the 20 transmittance is desired to be increased is λ , then the film thickness d of the silicon nitride film 22 that serves as the aforementioned supplementary capacitance use transparent insulating film is given by a thickness that satisfies the expression:

25
$$d = \lambda / (2 \times n) \times m \quad (m: \text{integer}).$$

Likewise, the pixel electrode 3 and the common electrode 21 are set to the thicknesses that provide the maximum transmittance.

That is, according to the aforementioned 5 embodiment, the film thicknesses of the pixel electrode 3 and the common electrode 21, which are made of ITO, and the silicon nitride film 22 are determined on the basis of the conditions:

refractive index of ITO: 1.9 to 2.0,
10 refractive index of silicon nitride: 1.9, and
wavelength at which transmittance is desired to be increased: 540 to 550 nm.

The green color (550 nm) is thus considered important in determining the film thickness because the 15 human eye is sensitive to this wavelength region and because the values of illuminance and white color transmittance are determined on the basis of the human visibility. Of course, the transmittance is desired to be high at R (Red), G (Green) and B (Blue) since the colorific taste of the white 20 color shifts if only the transmittance of green color is high. However, the film thickness that satisfies all these factors is not practical with this construction.

As described above, the pixel electrode 3 and the common electrode 21, which are made of ITO that is the 25 transparent conductive film, overlap each other to form the

supplementary capacitance via the silicon nitride film 22 that serves as the supplementary capacitance use transparent insulating film. Therefore, by maintaining the aperture rate without obstructing the display and optimizing the film 5 thicknesses of the pixel electrode 3 and the common electrode 21 so that the transmittance becomes high with respect to light at the predetermined wavelength of 540 nm to 550 nm for the formation of the pixel electrode 3 and the common electrode 21 into high-quality films that absorb a 10 small quantity of light, the loss of transmission light is restrained. The above arrangement can increase the supplementary capacitance without reducing the aperture rate of the pixel and increases the light utilization efficiency by optimizing the transmittance at the opening. Therefore, 15 a liquid crystal display device having an excellent display quality capable of obtaining a sufficient supplementary capacitance without reducing the light utilization efficiency can be provided even for a high-definition panel.

By setting the refractive index of the 20 supplementary capacitance use transparent insulating film (silicon nitride film 22) to a value of not smaller than 1.4 and setting a difference in refractive index between the supplementary capacitance use transparent insulating film 22 and the pixel electrode 3 and a difference in refractive 25 index between the supplementary capacitance use transparent

insulating film 22 and the common electrode 21 to a value of not greater than 0.6, the film thicknesses can be easily controlled as follows so as to provide a high transmittance by interference.

5 A transmission ratio T and a reflectance ratio R at the interface with respect to a perpendicular incident light are as follows:

$$R = (n_1 - n_0)^2 / (n_1 + n_0)^2$$

$$T = 1 - R$$

10 when the light is incident from a medium 0 (refractive index n_0) on a medium 1 (refractive index n_1). In this case, if the difference between n_1 and n_0 is small, then the reflectance is small and the transmittance is great.

15 Fig. 4 shows the result of calculation of a supplementary capacitance use transparent insulating film held between ITO's ($n = 2.0$) on the assumption that the interfacial reflection is entirely the loss. If the difference in refractive index is 0.6, then the transmittance is reduced by about 5% (with respect to 100% when the difference is zero).

20 Practically, in the case of a thin film, the reflected light is reflected on another interface, causing interference. The transmittance depending on the calculated thickness is shown in Fig. 5. As described above, the transmittance varies depending on the thickness and the

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refractive index. However, in the actual films, the interfaces of the films are not completely parallel, and diffusion and the like occur at the interfaces. Therefore, in practice, a transmittance of the combination of Fig. 4 and Fig. 5 results.

The optimum refractive index varies in relation to the interference. However, if the refractive index difference exceeds 0.6, then the width of fluctuation with respect to the film thickness becomes great, resulting in difficulties in obtaining stable characteristics.

If the refractive index difference is small, then the reflection on the interface itself is reduced. This consequently reduces the influence of the interfacial structure and so on and allows stable light transmission characteristics to be obtained.

By forming the pixel electrode 3 and the common electrode 21 of a material having a specific resistance of not greater than $1 \text{ m}\Omega\cdot\text{cm}$, no bad influence is exerted on the display due to the possible occurrence of a potential difference inside the screen because of the high resistance.

The edge portions of the pixel electrodes 3 are made to overlap the signal lines 2 (source bus lines) and the gate bus lines 1, so that the boundaries between mutually adjoining pixel electrodes 3 are provided on the signal lines 2 and the gate bus lines 1. The common

electrode 21 is arranged between the gate bus lines 1 and the pixel electrodes 3 and between the signal lines 2 and the pixel electrodes 3 so as to cover the gate bus lines 1 and the signal lines 2. With the above arrangement, the 5 signal lines 2 and the gate bus lines 1 can be used as a black matrix. This obviates the need for separately forming a black matrix and is therefore able to eliminate the black matrix forming process, reducing the cost. Furthermore, the common electrode 21 exists between the gate bus lines 1 and 10 the pixel electrodes 3 and between the signal lines 2 and the pixel electrodes 3. With this arrangement, electric fields attributed to the signals of the gate bus lines 1 and the signal lines 2 are shielded by the common electrode 21, so that the potentials of the pixel electrodes 3 become hard 15 to receive the influence of the signals of the gate bus lines 1 and the signal lines 2, allowing an excellent display to be provided.

The silicon nitride film 22 is used for the supplementary capacitance use transparent insulating film. 20 However, if the flat insulating layer is formed of either the silicon oxide film or the organic resin film (acrylic resin, polyimide or the like) by the spin coating method or the like, then this arrangement is effective in terms of reducing the surface unevenness and uniforming the electric 25 fields to be applied to the liquid crystal molecules.

Furthermore, by forming the supplementary capacitance use transparent insulating film of a lamination of at least two of the silicon oxide film, the silicon nitride film and the organic resin film, both the dielectric properties and
5 flatness can be secured.

Furthermore, by using a high-mobility high-performance thin film transistor whose active layer is made of polysilicon for the pixel use thin film transistor and the drive circuit use thin film transistor, the pixel use
10 thin film transistor and the drive circuit use thin film transistor can be integrally formed on an inexpensive glass substrate. This arrangement obviates the need for separately providing a substrate for the drive circuit section constructed of an IC (Integrated Circuit) and an LSI
15 (Large Scale Integrated Circuit), allowing the manufacturing cost to be remarkably reduced.

Furthermore, by introducing a catalytic element (nickel, for example) and using the polysilicon film crystallized by utilizing its catalytic effect for the
20 active layer of the thin film transistor, the drive circuit section can be easily formed within a small area even in a high-definition panel.

In the case where a top gate type transistor (transistor having a structure in which the gate electrode
25 is located above the semiconductor layer) is used as the

pixel use transistor, if the principal light is made incident from above the insulating substrate (i.e., from the pixel electrode side), then the light can be prevented from being incident on the active layer under the gate electrode 5 of the pixel use thin film transistor, which is effective in terms of stabilizing the operation of the transistor. If a coating film made of a material of the layer identical to that of the source bus line is provided on the upper layer of the gate bus line in the portion where the active layer 10 of the thin film transistor and the gate bus line intersect each other, then the light shielding effect is further improved to allow the operation to be further stabilized.

Although ITO is used for the pixel electrodes 3 and the common electrode 21 in the aforementioned 15 embodiment, it is acceptable to use SnO_2 besides ITO or a material made principally of the materials.

The drain electrode 16 is dimensionally set so that the contact hole 20 can be connected to the drain electrode 16 in the aforementioned embodiment. However, by 20 forming a source electrode 40 expanded in area to the extent that the aperture rate is not reduced more than necessary as shown in Fig. 6, the supplementary capacitance is constructed of the source electrode 40, the common electrode 21 (shown in Fig. 2) and the resin film 18 (shown in Fig.

2), allowing the capabilities of retaining the potential of the pixel electrode to be improved.

Although the silicon oxide film 15 is formed as the first interlayer insulating film in the aforementioned embodiment, the film material is not limited to silicon oxide. Although the silicon nitride film 22 is formed as the supplementary capacitance use transparent insulating film, the supplementary capacitance use transparent insulating film may be formed of silicon oxide, aluminum oxide or the like.

As is apparent from the above, according to the liquid crystal display device of the present invention, if the supplementary capacitance is formed by utilizing the pixel electrode and the common electrode made of the transparent conductive films that overlap each other via the supplementary capacitance use transparent insulating film, then a sufficient supplementary capacitance can be formed without reducing the light utilization efficiency even in a high-definition panel. Furthermore, by forming the common electrode on the upper side of the bus lines so as to cover the lines, the common electrode shields the electric fields of the bus lines. With this arrangement, the potentials of the pixel electrodes become hard to receive the influence of the signals of the bus lines, allowing the display quality to be improved.

The invention being thus described, it will be obvious that the same may be varied in many ways. Such variations are not to be regarded as a departure from the spirit and scope of the invention, and all such 5 modifications as would be obvious to one skilled in the art are intended to be included within the scope of the following claims.

WHAT IS CLAIMED IS:

1. A liquid crystal display device comprising a thin film transistor array substrate including: pixel use thin film transistors, which are formed on an insulating substrate and each of which has a gate electrode, a source electrode and a drain electrode; pixel electrodes, which are formed on the insulating substrate and comprised of a transparent conductive film connected to the respective pixel use thin film transistors; and a supplementary capacitance for retaining electric charges of the pixel electrodes, and a liquid crystal layer held between the thin film transistor array substrate and an opposite substrate,

the supplementary capacitance being provided by the pixel electrodes, a supplementary capacitance use transparent insulating film formed under at least the pixel electrodes and a common electrode that is formed under the supplementary capacitance use transparent insulating film and comprised of a transparent conductive film connected to a specified potential, and

20 the pixel electrodes, the supplementary capacitance use transparent insulating film and the common electrode having a film thickness such that the electrodes and film have a transmittance increased by interference at a specified wavelength.

2. A liquid crystal display device as claimed in claim 1, wherein

a difference between a refractive index of the supplementary capacitance use transparent insulating film and a refractive index of the pixel electrodes is set to a value of not greater than 0.6 and a difference between a refractive index of the supplementary capacitance use transparent insulating film and a refractive index of the common electrode is set to a value of not greater than 0.6.

10 3. A liquid crystal display device as claimed in claim 1, wherein

the pixel electrode and the common electrode are made of a material having a specific resistance of 1 mΩ·cm or less.

15 4. A liquid crystal display device as claimed in claim 1, wherein

the pixel electrodes have edge portions overlapping gate bus lines and source bus lines formed on the insulating substrate, and

20 the common electrode is arranged between the gate bus lines and the pixel electrodes and between the source bus lines and the pixel electrodes so as to cover the gate bus lines and the source bus lines.

25 5. A liquid crystal display device as claimed in claim 1, wherein

the supplementary capacitance use transparent insulating film is any one of a silicon oxide film, a silicon nitride film and an organic resin film or a laminate film comprised of at least two of the silicon oxide film,
5 the silicon nitride film and the organic resin film.

6. A liquid crystal display device as claimed in claim 1, wherein

the pixel use thin film transistor has an active layer made of polysilicon, and

10 drive circuit use thin film transistors whose active layers are made of polysilicon are formed on the insulating substrate identical to the substrate on which the pixel use thin film transistors are formed.

7. A liquid crystal display device as claimed in
15 claim 6, wherein

the active layers of the pixel use thin film transistors and the drive circuit use thin film transistors are polysilicon films crystallized by utilizing a catalytic effect of an introduced catalytic element.

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ABSTRACT OF THE DISCLOSURE

There is provided a liquid crystal display device that can obtain a high light utilization efficiency and a sufficient supplementary capacitance without reducing an aperture rate of pixels and is able to attain higher resolution. A silicon nitride film 22 that serves as a supplementary capacitance use transparent insulating film is formed under a pixel electrode 3. A common electrode 21 that is made of ITO and is connected to a potential common to an opposite electrode is formed under the silicon nitride film 22. The pixel electrode 3, the silicon nitride film 22 and the common electrode 21 constitute the supplementary capacitance, and the pixel electrode 3, the silicon nitride film 22 and the common electrode 21 are each made to have a film thickness such that a transmittance is increased by interference at a specified wavelength.

Fig. 1

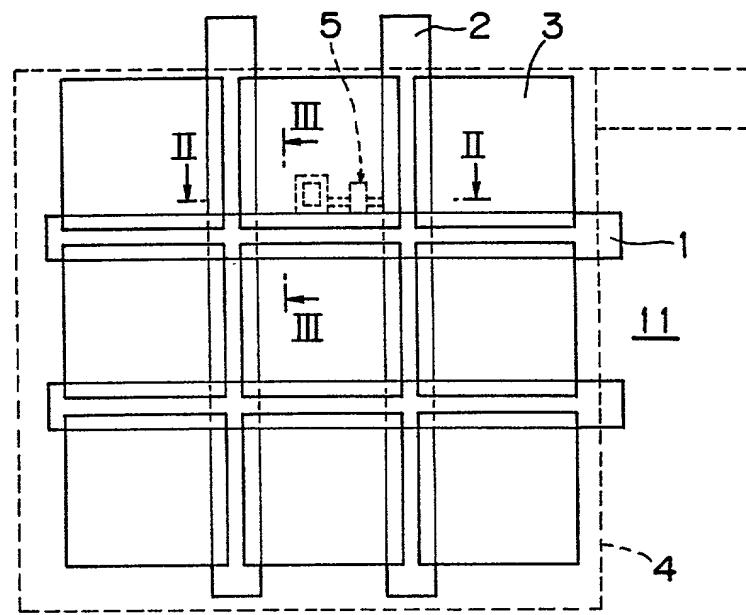


Fig. 2

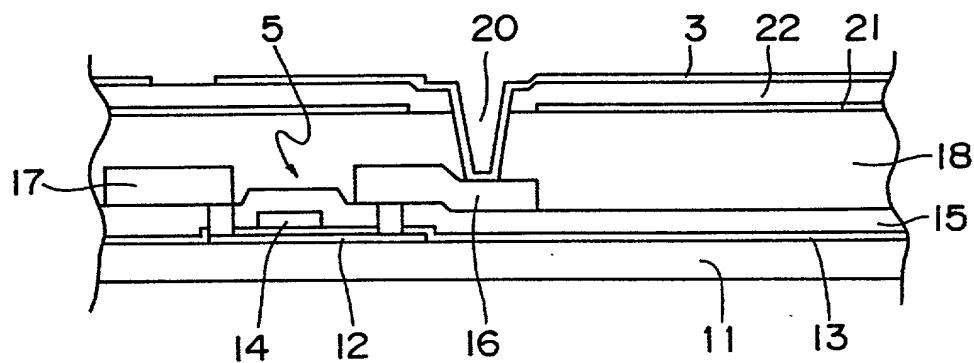


Fig. 3

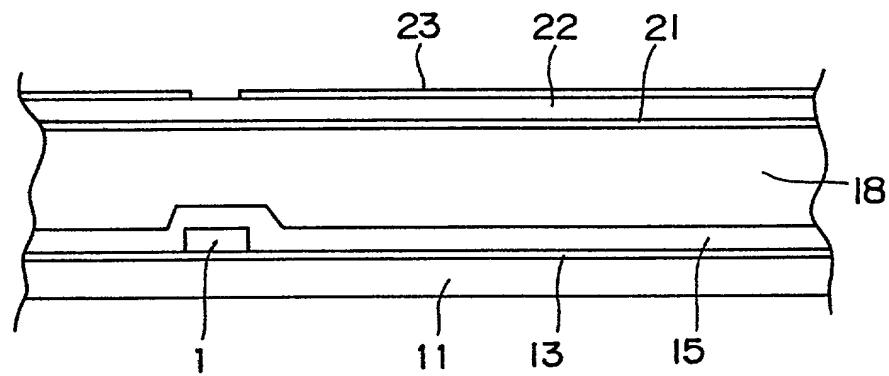
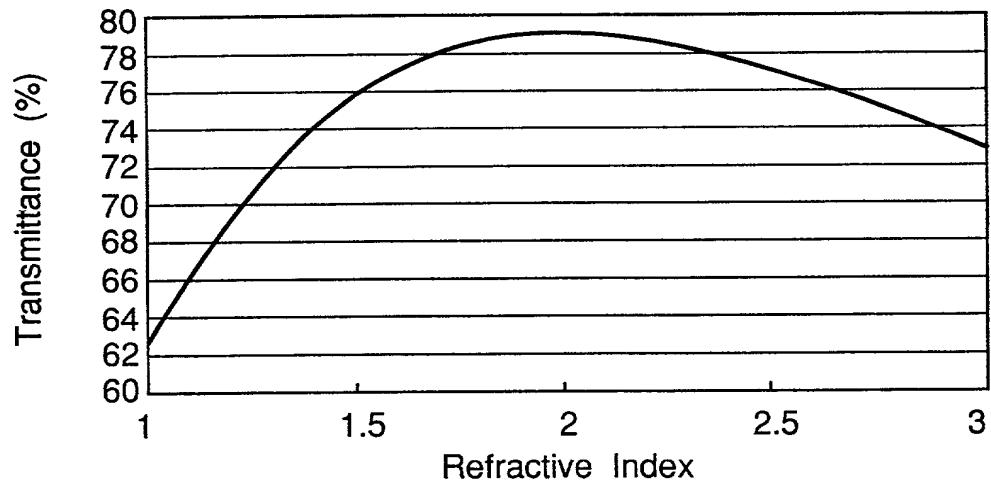


Fig.4



DATA FROM DEPT 360

Fig.5

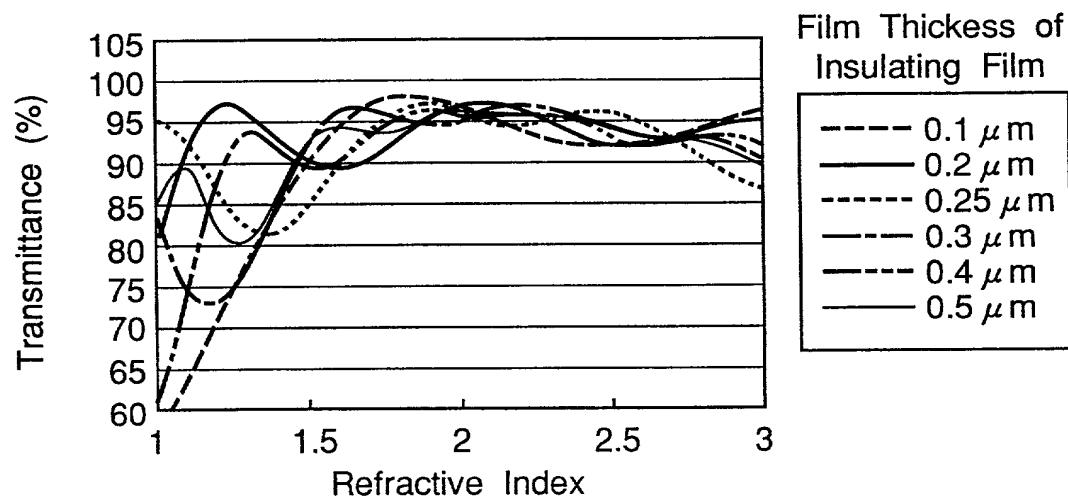


Fig . 6

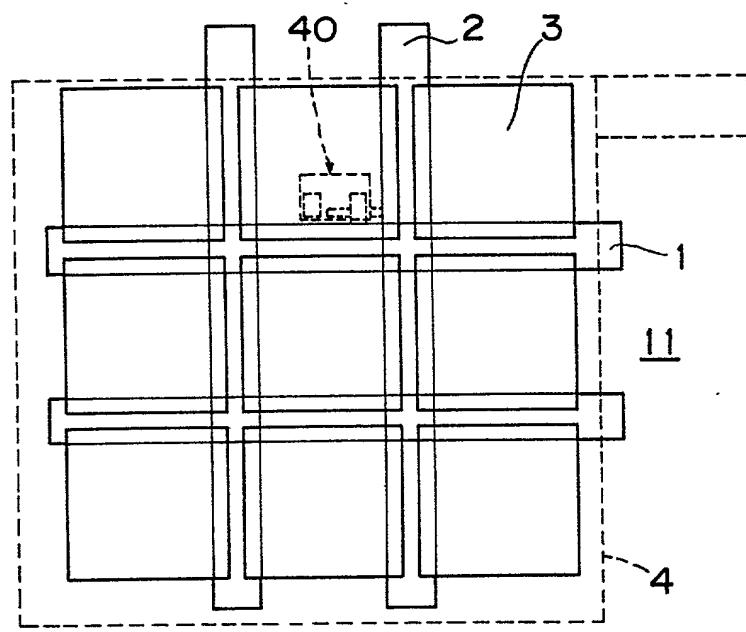
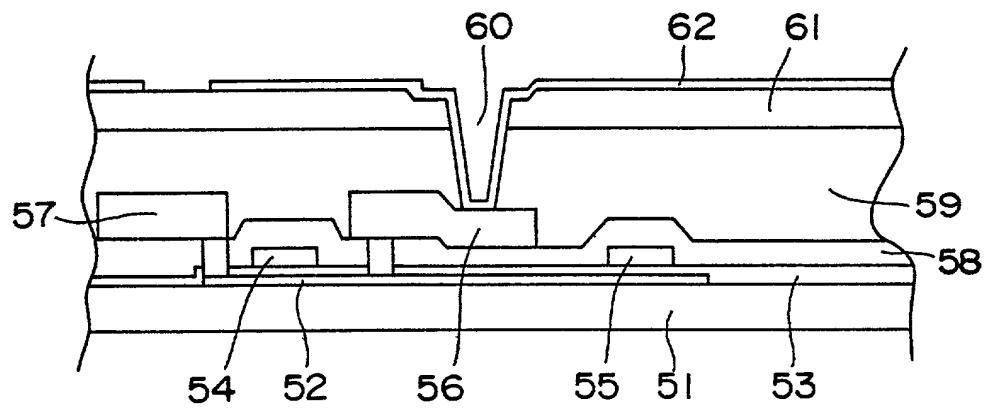


Fig. 7



RULE 63 (37 C.F.R. 1.63)
DECLARATION AND POWER OF ATTORNEY
FOR PATENT APPLICATION
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name, and I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

LIQUID CRYSTAL DISPLAY DEVICE HAVING HIGH LIGHT UTILIZATION EFFICIENCY

the specification of which (check applicable box(s)):

is attached hereto
 was filed on _____ as U.S. Application Serial No. _____ (Atty Dkt. No. _____)
 was filed as PCT International application No. _____ on _____
and (if applicable to U.S. or PCT application) was amended on _____

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with 37 C.F.R. 1.56. I hereby claim foreign priority benefits under 35 U.S.C. 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed or, if no priority is claimed, before the filing date of this application:

Priority Foreign Application(s):

Application Number	Country	Day/Month/Year Filed
11-206090	Japan	21/07/1999

I hereby claim the benefit under 35 U.S.C. §119(e) of any United States provisional application(s) listed below.
Application Number Date/Month/Year Filed

I hereby claim the benefit under 35 U.S.C. 120/365 of all prior United States and PCT international applications listed above or below and, insofar as the subject matter of each of the claims of this application is not disclosed in such prior applications in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose material information as defined in 37 C.F.R. 1.56 which occurred between the filing date of the prior applications and the national or PCT international filing date of this application:

Prior U.S./PCT Application(s): Application Serial No. Day/Month/Year Filed Status: patented pending, abandoned

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon. And on behalf of the owner(s) hereof, I hereby appoint **NIXON & VANDERHYE P.C., 1100 North Glebe Rd., 8th Floor, Arlington, VA 22201-4714, telephone number (703) 816-4000 (to whom all communications are to be directed)**, and the following attorneys thereof (of the same address) individually and collectively owner's/owners' attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith and with the resulting patent: Arthur R. Crawford, 25327; Larry S. Nixon, 25640; Robert A. Vanderhye, 27076; James T. Hosmer, 30184; Robert W. Faris, 31352; Richard G. Basha, 22770; Mark E. Nusbaum, 32348; Michael J. Keenan, 32106; Bryan H. Davidson, 30251; Stanley C. Spooner, 27393; Leonard C. Mitchard, 29009; Duane M. Byers, 33363; Jeffry H. Nelson, 30481; John R. Lastova, 33149; H. Warren Burnam, Jr. 29366; Thomas E. Byrne, 32205; Mary J. Wilson, 32955; J. Scott Davidson, 33489; Alan M. Kagen, 36178; Robert A. Molan, 29834; B. J. Sadoff, 36663; James D. Berquist, 34776; Updeep S. Gill, 37334; Michael J. Shea, 34725; Donald L. Jackson, 41090; Michelle N. Lester, 32331; Frank P. Presta, 19828; Joseph S. Presta, 35329 I also authorize Nixon & Vanderhye to delete any attorney names/numbers no longer with the firm and to act and rely solely on instructions directly communicated from the person, assignee, attorney, firm, or other organization sending instructions to Nixon & Vanderhye on behalf of the owner(s).

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	Inventor: _____			(citizenship)
	Residence: (city) _____			
	Post Office Address: _____			
	(Zip Code)			

FOR ADDITIONAL INVENTORS, check box and attach sheet with same information and signature and date for each.